

What is claimed is:

1. A multiple-processor information processing system including a plurality of processor modules,  
5 the information processing system comprising:  
virtual IP address definition means for defining  
virtual IP addresses on a process module-by-processor  
module basis;  
storage means for storing said virtual IP addresses  
10 defined by said virtual IP address definition means and  
information indicative of ones of said processor modules  
corresponding to said virtual IP addresses, respectively,  
in a state correlated with each other; and  
notification means for notifying a router of a  
15 virtual IP address of each processor module and a real IP  
address of said each process module as routing information,  
for said each processor module having said virtual IP  
address stored in said storage means.
- 20 2. A multiple-processor information processing  
system according to claim 1, wherein said notification  
means notifies said router of a destination IP address of  
each processor module and a real IP address of said each  
processor module as routing information, for said each  
25 processor module having no corresponding virtual IP  
address stored in said storage means.

3. A recording medium readable by a multiple-processor information processing system including a plurality of processor modules, said recording medium storing a program for causing said multiple-processor  
5 information processing system to carry out a process for notification of routing information,

said program causing multiple-processor information processing system to function as:

virtual IP address definition means for defining  
10 virtual IP addresses on a process module-by-processor module basis;

storage means for storing said virtual IP addresses defined by said virtual IP address definition means and information indicative of ones of said processor modules  
15 corresponding to said virtual IP addresses, respectively, in a state correlated with each other; and

notification means for notifying a router of a virtual IP address of each processor module and a real IP address of said each process module as routing information,  
20 for said each processor module having said virtual IP address stored in said storage means.